

AMENDMENTS TO THE SPECIFICATION:

Please replace the paragraph beginning on page 3, line 2, with the following:

[0007] In the present invention, a receiver comprises analog-to-digital circuitry for generating a digital representation of a signal at an input and adjustable gain control circuitry for receiving a radio signal and outputting an amplified analog signal using a gain determined by a magnitude of the signal at the ~~output~~input of the analog-to-digital circuitry. Digital channel filtering circuitry filters the digital representation and digital processing circuitry processes the output of the digital representation.

Please replace the paragraph beginning on page 3, line 9, with the following:

[0008] The present invention provides significant advantages over the prior art. By initiating gain changes in the automatic gain control circuit responsive to the signal at the ~~output~~input of the analog-to-digital circuit (where the signal may be the superposition of the useful signal, DC offset, adjacent channels and blockers), the gain control implementation permits optimal use of the analog-to-digital converter's dynamic range. The present invention allows reductions in gain only when necessary, with subsequent optimization of receiver sensitivity.

Please replace the paragraph beginning on page 7, line 3, with the following:

[0020] In operation, the AGCs 32 adjust gain responsive to the magnitude of the signal ~~output from~~input to one of the ADCs 22, prior to filtering by the digital channel filter 24.

The magnitude of the input signal to the ADC 22 may be determined using a set of most significant bits of the ADC output. Depending upon the urgency of the gain adjustment (i.e., depending upon how close the ADC is to saturation), the gain adjustment may be made directly by the AGC 32 without DSP intervention. For example, the gain may be reduced automatically by the AGC 32 whenever the most significant bit of the ADC output is set to "1" and may be reduced or increased responsive to control signal from the DSP in other circumstances.